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Electronic Version v1.1

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**Title of
Invention****[SPLIT-GATE FLASH MEMORY STRUCTURE AND METHOD OF
MANUFACTURE]**

Application Number :

Date :

First Named Applicant: Mr. Ko-Hsing Chang

Confirmation Number:

Attorney Docket Number: 9134-US-PA-1

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Submitted By:**Elec. Sign.****Sign. Capacity**

Dr. Belinda Lee

/Belinda Lee/

Agent

Registered Number: 46,863

Documents being submitted:	Files
us-request	9134-D-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	9134-D-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	9134declaration1.tif
us-declaration	9134declaration2.tif
application-body	9134-D-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd 9134us0001.tif 9134us0002.tif 9134us0003.tif 9134us0004.tif 9134us0005.tif 9134us0006.tif 9134us0007.tif 9134us0008.tif 9134us0009.tif 9134us0010.tif 9134us0011.tif
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